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Digital Logic Design is used to develop hardware, such as circuit boards and microchip processors. This hardware processes user input, system protocol and other data in computers, navigational systems, cell phones or other high-tech systems.

### **Digital Logic Design - University of California, Davis**

2.Advanced Digital Logic Design using Verilog, State Machine & Synthesis for FPGA â€“ Sunggu Lee, Cengage Learning , 2012. ... Course Objectives ( as per JNTU-H) This course teaches: Designing digital circuits, behavioral and RTL modeling of digital circuits using Verilog HDL.

### **DDVH COURSE FILE - geethanjaliinstitutions.com**

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JNTU Digital logic design CSE Syllabus . JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD II Year B.Tech. CSE â€“ I Sem T P C 4+1\* 0 4 DIGITAL LOGIC DESIGN. UNIT-I BINARY SYSTEMS : Digital Systems, Binary Numbers, Number base conversions, Octal and Hexadecimal Numbers, complements, Signed binary numbers, Binary codes, Binary Storage and Registers, Binary logic.

### **JNTU Digital logic design CSE Syllabus - kopykitab.com**

Digital Logic Design BiBasics Combinational Circuits Sequential Circuits Pu-Jen Cheng Adapted from the slides prepared by S. Dandamudi for the book, Fundamentals of Computer Organization and Design.

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